



PATENT APPLICATION  
042390.P3275RC1

CLEAN VERSION OF CLAIMS FOR SCANNING PER 37 CFR § 1.101 RECEIVED

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D1 9/4/1  
7. An apparatus, comprising:  
a static random access memory;  
a processor coupled to the static random access memory;  
a voltage regulator adapted to provide at least two voltage potential levels to at least a portion of the processor; and  
wherein the voltage potential level provided by the voltage regulator is adapted to be adjusted depending on a frequency corresponding to the operational load of the processor.

D2  
14. A method of operating a processor, comprising:  
monitoring an application mix executed by a processor; and  
adjusting the voltage potential level provided to at least a portion of the processor based on the application mix executed by the processor.

D3  
17. An article comprising: a storage medium having stored thereon instructions, that, when executed by a computing platform, results in:  
monitoring an application mix executed by a processor; and  
adjusting the voltage potential level provided to at least a portion of the computing platform based on the application mix executed by the computing platform.

D4  
25. An apparatus, comprising:  
a processor;  
a voltage regulator adapted to provide at least two voltage potential levels to at least a portion of the processor; and  
wherein the voltage potential level provided by the voltage regulator is adapted to be adjusted depending on the operational load of the processor, and is provided to an electrically common terminal in the processor.

D5  
32. A method comprising:  
determining a frequency for an application mix and providing a voltage potential for the frequency.

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05  
33. The method of claim 32, wherein providing a voltage potential includes providing a voltage potential to a core of a processor.

34. The method of claim 33, wherein providing a voltage potential includes providing a different voltage potential to a pad of the processor.

35. An apparatus comprising:  
a state machine adapted to determine a frequency for an application mix, wherein the state machine is further adapted to provide a voltage potential to at least a core of a processor for the frequency.

36. The apparatus of claim 35, further comprising a voltage regulator to provide the voltage potential level.

37. The apparatus of claim 35, wherein the processor is adapted to receive a clock signal that is varied in accordance with changes in the application mix of the processor.

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